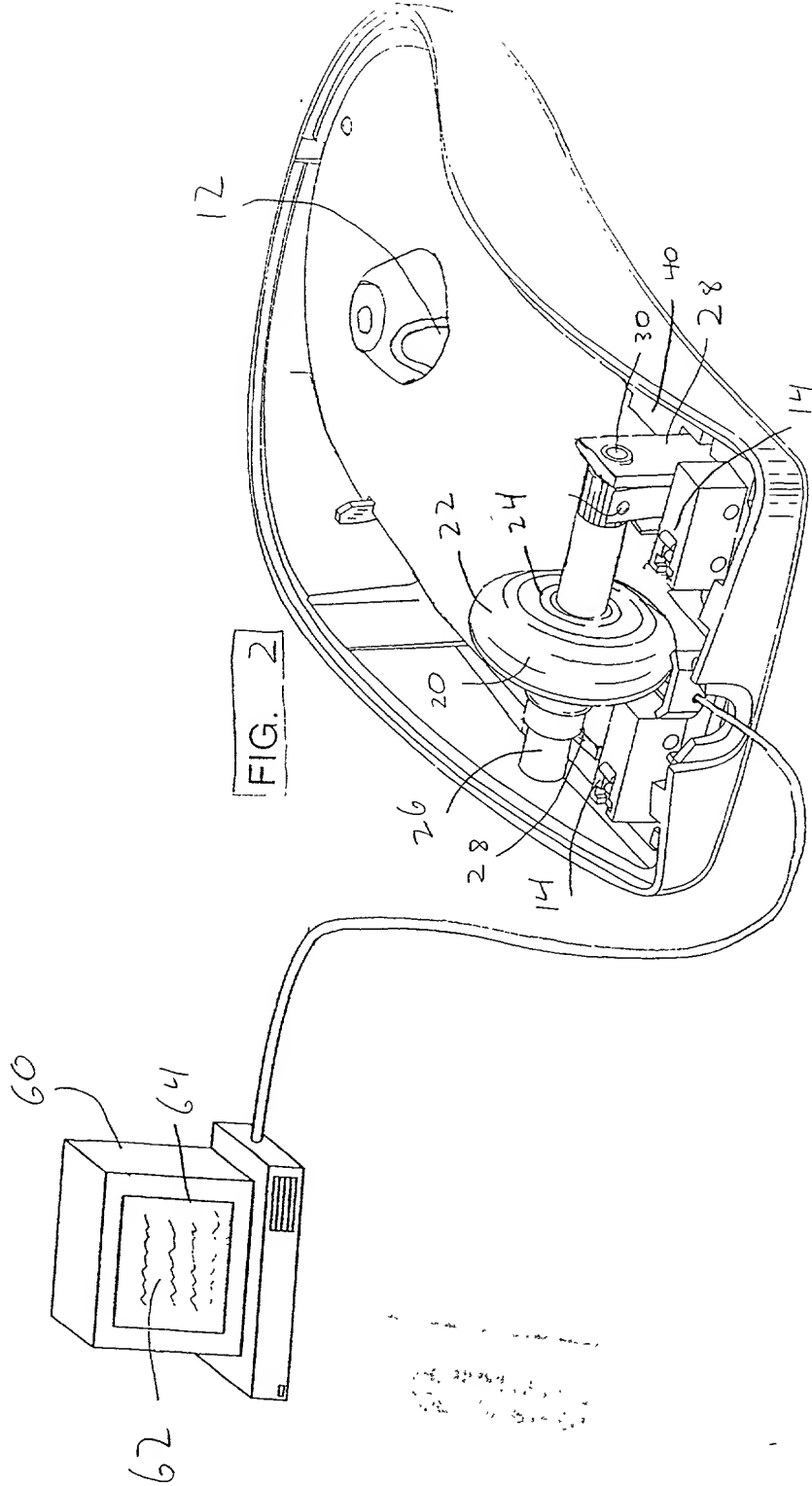


FIG 1



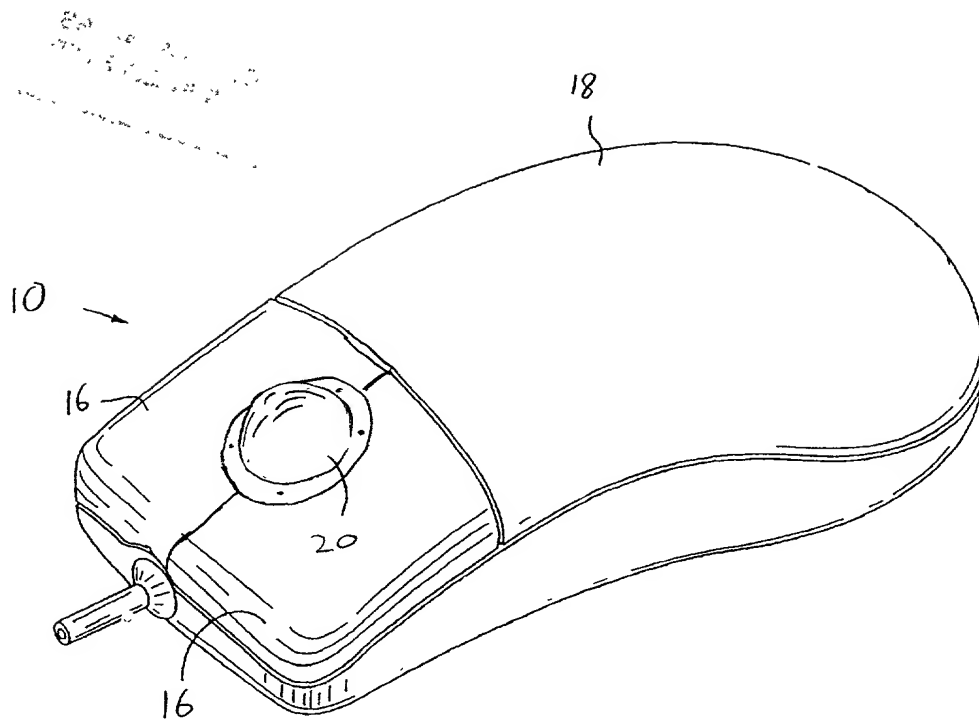


FIG. 3

FIG. 4 is a block diagram of a system for motion signal processing.

40

44

Timer

42

Memory  
Buffer

Input  
from  
Motion  
Signal  
Generator

48

Output Signal  
Generator

Output to  
Software  
Driver

46

FIG. 4

FIG. 4 is a block diagram of a system for motion signal processing.

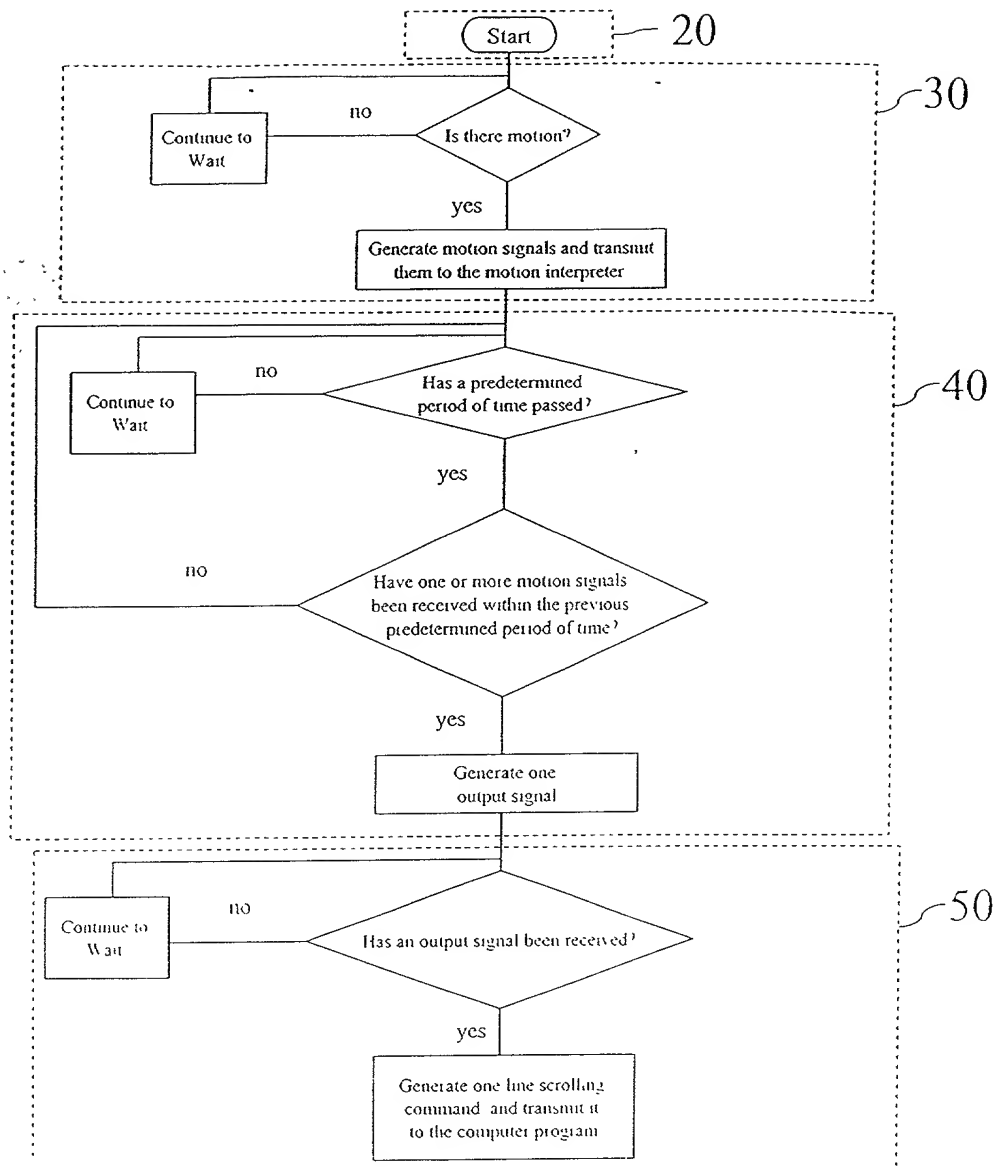


FIG. 5